



ATTORNEY DOCKET No. 9898-204

C. Wills

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Jae-Yoon SIM and Jei-Hwan YOO

Serial No.: 09/901,930

Filing Date: July 9, 2001

Title: NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR MEMORY DEVICE

Box DD
ASSISTANT COMMISSIONER FOR PATENTS
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INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97 (b), or
(Within three months of filing national application; or date of entry of International application;
or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97 (e), or
☐ a \$180 fee under 37 CFR 1.17 (p), or
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action).
- ☐ under 37 CFR 1.97 (d) together with either:
☐ Statement under 37 CFR 1.97 (e),
☐ Applicant hereby petitions for the consideration of the accompanying
information disclosure statement (37 C.F.R. §1.97(d)(ii).
☐ a \$130.00 petition fee set forth in 37 CFR 1.17 (i)(1).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of

the issue fee)

☐ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with
copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s)
may be material to the examination of this application and for which there may be a duty to disclose in accordance
with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A
duplicate copy of this sheet is enclosed.



20575

PATENT TRADEMARK OFFICE

Respectfully submitted,

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Atty. Docket No.: 9898-204

Serial No. 09/901,930

Applicant: Jae-Yoon SIM and Jei-Hwan YOO

Filing Date: July 9, 2001

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)U.S. PATENT DOCUMENTS

| <u>Exam Init</u> | <u>Ref</u> | <u>Document Number</u> | <u>Issue Date</u> | <u>Name</u> | <u>Class</u> | <u>Sub- Class</u> |
|----------------------|------------|----------------------------|-----------------------|------------------|--------------|-----------------------|
| _____ | _____ | 6,097,665 | August 1, 2000 | Tomishima et al. | | |
| _____ | _____ | 6,147,914 | Nov. 14, 2000 | Leung et al.. | | |

FOREIGN PATENT DOCUMENTS

| <u>Document Number</u> | <u>Publication Date</u> | <u>Country</u> | <u>Name</u> |
|----------------------------|-----------------------------|----------------|-------------|
| _____ | _____ | _____ | _____ |
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OTHER DOCUMENTS

| <u>Exam Init</u> | <u>Ref</u> | <u>(including Author, Title, Date, Pertinent Pages, etc.)</u> |
|----------------------|------------|--|
| _____ | _____ | 1998 Symposium on VLSI Circuits Digest of Technical Papers P94-95 "A precise On-chip voltage Generator for a Giga-Scale DRAM with a Negative Word-line Scheme" |
| _____ | _____ | |
| _____ | _____ | |

Examiner: _____

Date Considered: _____